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	Application Number	09/945121	
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	First Named Inventor	Shirley, Paul	
	Group Art Unit	1744	
	Examiner Name	Till, Terrence	
21 1 4 - 4 4	Attorney Docket No: 3	303.774US1	

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Examiner Initial *	USP Document Number	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	Filing Date If Appropriate
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		FOREIGN PATENT	DOCUMENTS			
Examiner Initials*	Foreign Document No	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	T²

	OTHER	R DOCUMENTS NON PATENT LITERATURE DOCUMENTS	
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B		VAN ZANT, PETER, "Microchip fabrication: a practical guide to semiconductor processing", 4th Edition, New York: McGraw-Hill, Chapter 4 & Chapters 8-9,(2000),87-132 & 193-280	